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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/843,533	04/25/2001	William Fornaciari	851763.406	7410
500	500 7590 12/02/2003		EXAMINER	
SEED INTELLECTUAL PROPERTY LAW GROUP PLLC			HUYNH, KIM T	
701 FIFTH AVE SUITE 6300		ART UNIT	PAPER NUMBER	
SEATTLE, WA 98104-7092			2189.	
			DATE MAILED: 12/02/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/843,533	FORNACIARI ET AL.				
Office Action Summary	Examiner	Art Unit				
	Kim T. Huynh	2189				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM						
THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status						
1)⊠ Responsive to communication(s) filed on 25 A	April 2001.					
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3) Since this application is in condition for allowa	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1-30 is/are pending in the application	☑ Claim(s) <u>1-30</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdra	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.	Claim(s) is/are allowed.					
6) Claim(s) <u>1-4</u> , <u>15-19</u> , <u>20-22</u> , <u>27</u> is/are rejected.						
7) Claim(s) <u>5-14,23-26 and 28-30</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10)⊠ The drawing(s) filed on <u>25 April 2001</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
<u> </u>	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. §§ 119 and 120						
 12) △ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) △ All b) ☐ Some * c) ☐ None of: 1. △ Certified copies of the priority documents have been received. 2. ☐ Certified copies of the priority documents have been received in Application No 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78. a) ☐ The translation of the foreign language provisional application has been received. 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78. 						
·						
Attachment(s)						
Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Other:						

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-4, 15-19, 21-22, 27 are rejected under 35 U.S.C. 102(e) as being anticipated by Martwick (US Patent 6,336,158)

As per claims 1, 15, 22, Martwick discloses a circuit architecture for buses, comprising: encoder/decoder architecture for buses, capable of receiving a current value of input information relating to a given instant and of generating, from this current input value, a corresponding output value relating to the same given instant on encoded bus lines, the encoder/decoder architecture comprising: (col.7, lines 6-23)

- At least one memory element for storing the respective preceding input information value and output information value, (col.8, line 55-col.9, line 28)
- A prediction block for generating an estimate of the current input information value on the basis of the preceding input information value, and (col.7, lines 5-23), (col.8, line 55-col.9, line 28)

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- A decorrelation block for decorrelating the current input information value with respect to the estimate, to produce a decorrelation result, (col.5, lines 37-67)
- The current output value adapted to be selected as one of the following:
 - o The current input information value, (col.8, line 55-col.9, line 12)
 - The preceding output value, and (col.8, line 55-col.9, line 12)
 - o The decorrelation result. (col.8, line 55-col.9, line 12)

As per claim 2, Martwick discloses the archituecture comprising a selection block for selecting the current output value. (col.9, lines 13-29)

As per claim 3, Martwick discloses wherein the at least one memory element comprises corresponding registers for storing the corresponding preceding input information values and output information values. (col. 6, lines 13-51)

As per claim 4, martwick discloses wherein at least one of the blocks is at least partially implemented by means of pass-gates. (col.9, lines 13-29)

As per claim 16, Martwick discloses wherein the at least one bus interface operates at sub-system level. (col.5, lines 2-37)

As per claim 17, Martwick discloses wherein the at least one bus interface operates at the processor-to-cache bus level. (col.3, lines 58-65), (col.6, lines 31-51)

As per claim 18, Martwick discloses wherein the at least one bus interface operates at system level. (col.4, line 48-col.5, 20)

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As per claim 19, Martwick discloses configured in the form of a shared memory multiprocessor system. (col.3, lines 47-65)

As per claim 21, Martwick discloses the system comprising a structure of the multichip type. (fig.1, wherein each block may integrated a single chip, many block formed multichip type)

As per claim 27, Martwick discloses wherein the prediction block comprises an identity module, and the decorrelation block comprises a difference module configured to also select the current output value. (col.8, line 55-col.9, line 12), (col.5, lines 37-67)

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Martwick (US Patent 6,336,158)

Martwick discloses all the limitations as above except fails to disclose the system comprising a structure of the monolithic type.

Examiner takes official notice that monolithic type structure of the system are well known in the art. It would have been obvious one having ordinary skills

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in the art at the time the invention was made to include monolithic type to form single type of substrate material so as to be compatible in the multichip system.

CLAIMS OBJECTION

5. Claims 5-14, 23-26, 28-30 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Prior art fails to teach or suggest an XOR logic gate, acting as a decorrelation block and acting as a selection block capable of selecting the current output value.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kim Huynh whose telephone number is (703)305-5384 or via e-mail addressed to [kim.huynh3@uspto.gov]. The examiner can normally be reached on M-F 8:30AM- 6:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on (703) 305-4815 or via e-mail addressed to [mark.rinehart@uspto.gov]. The fax phone numbers for the organization where this application or proceeding is assigned are (703)872-9306 for regular communications and After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)306-5631.

Kim Huynh

Nov. 23, 2003

MARK H. RINEHART
DERINGCEY PATENT EXAMINER
DERINGCEY PATENT EXAMINER